EE/CprE/SE 491 WEEKLY REPORT 07

10/25/24 - 10/31/24

Group number: sdmay25-31

Project title: i281

Client & Advisor: Prof. Alexander Stoytchev

Team Members:

Ethan Uhrich - Team Lead & Treasurer Ariana Dirksen - Editor & Note Taker

Tessa Morgan - Task Manager & Webmaster

Gigi Harrabi - Client Interaction & Outreach Coordinator

Weekly Summary

The focus of this week was originally to begin or finish miscellaneous tasks. However after meeting with our client it was determined that we needed to clean up the wires in our hardware designs and redesign Ethan/Ariana's implementation of the PC. We also continued our work on the First Lab's design and implementation.

Past Week Accomplishments

Ethan Uhrich: Rewire/Redesign PC, rewire Tester and Mux

Ariana Dirksen: Redesign and rewire PC, rewire Tester and MUX implementations. Cut wires for

hardware implementations. Added select lines to the Tester implementation.

Tessa Morgan: Got feedback from Stoytchev on PC layout and implemented the changes he

suggested. Created a journey diagram and finalized MUX lab activity descriptions.

Gigi Harrabi: Finalized MUX lab

Individual Contributions

NAME	Individual Contributions (Quick list of contributions. This should be short.)	Hours this week	HOURS cumulative
Ethan Uhrich	Rewired PC, MUX and PC	4	33
Ariana Dirksen	Redesign and rewire PC, rewire Tester and MUX implementations.	4	37
Tessa Morgan	MUX Lab draft, and finalized MUX simulations. Finished PC circuit.	4	33
Gigi Harrabi	Implemented MUX Lab draft	4	31

Plans for the Upcoming Week

Ethan Uhrich: Finish Rewiring mux and PC

Ariana Dirksen: Finish Rewiring MUX and PC, Make edits to Lab 3, Continue Lab 1

Tessa Morgan: Get additional feedback on lab documentation and existing simulations and start

simulating PC.

Gigi Harrabi: Make a draft document for WiSE

Summary of Weekly Advisor Meeting

- Circuit Check MUX

- Going Over Rough Draft Lab 3
 - What to Add
 - What to Improve
- Continued Standardization Talks
 - Now that we have cut wire, standardizing colors, inputs, outputs
- Circuit Check PC
- Discussion on Lab 4
 - Additional Requirements Added
- Cost and Wire Cutting vs Pre-Cut Wires Discussion